

.REM -

SFQ 0001

-----  
IDENTIFICATION  
-----

PRODUCT CODE: AC-E739B-MC  
PRODUCT NAME: CXDRFB0 DRV11B MODULE  
PRODUCT DATE: SEPTEMBER 1978  
MAINTAINER: DEC/X11 SUPPORT GROUP

THE INFORMATION IN THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE AND SHOULD NOT BE CONSTRUED AS A COMMITMENT BY DIGITAL EQUIPMENT CORPORATION. DIGITAL EQUIPMENT CORPORATION ASSUMES NO RESPONSIBILITY FOR ANY ERRORS THAT MAY APPEAR IN THIS MANUAL.

THE SOFTWARE DESCRIBED IN THIS DOCUMENT IS FURNISHED TO THE PURCHASER UNDER A LICENSE FOR USE ON A SINGLE COMPUTER SYSTEM AND CAN BE COPIED (WITH INCLUSION OF DIGITALS COPYRIGHT NOTICE) ONLY FOR USE IN SUCH SYSTEM, EXCEPT AS MAY OTHERWISE BE PROVIDED IN WRITING BY DIGITAL.

DIGITAL EQUIPMENT CORPORATION ASSUMES NO RESPONSIBILITY FOR THE USE OR RELIABILITY OF ITS SOFTWARE ON EQUIPMENT THAT IS NOT SUPPLIED BY DIGITAL.

COPYRIGHT (C) 1976,1978 DIGITAL EQUIPMENT CORPORATION

1. ABSTRACT

"DRF" IS AN "TOMODX" THAT EXERCISES ONLY ONE DRV11B. IF THE SYSTEM CONTAINS MORE THAN ONE DRV11B, ANOTHER COPY OF THIS MODULE MUST BE LOADED. A READ/WRITE REGISTER TEST IS PERFORMED TO ENSURE SOME OPERATING CONFIDENCE IN THE BASIC HARDWARE INTERFACE. THE DRV11B IS THEN ENABLED TO PERFORM DMA TRANSFERS TO AND FROM MEMORY USING DIFFERENT MODES AND DATA PATTERNS.

2. REQUIREMENTS

HARDWARE: DRV11B INTERPROCESSOR INTERFACE

STORAGE:: DRF REQUIRES:

1. DECIMAL WORDS: 1489
2. OCTAL WORDS: 92721
3. OCTAL BYTES: 5642

3. PASS DEFINITION

ONE PASS OF DRF MODULE CONSISTS OF 3072 PROGRAM INTERRUPTS AND 204,800 NON-PROCESSOR REQUESTS.

4. EXECUTION TIME

DRF RUNNING ALONE ON LSI-11 TAKES APPROXIMATELY ONE MINUTE.

5. CONFIGURATION REQUIREMENTS

DEFAULT PARAMETERS:

DEVADR: 172410, VECTOR: 124, BRI: 4, DEVCNT: 1, SRI: 0

REQUIRED PARAMETERS:

NONE, THIS MODULE ONLY SUPPORTS ONE DRV11B.

6. DEVICE/OPTION SETUP

THE MAINTENANCE "WRAP-AROUND" BERG CABLE MUST BE INSTALLED.

7. MODULE OPERATION  
-----

THE MODULE WILL BEGIN BY TESTING THE ABILITY OF THE BUS READ/WRITE  
REGISTERS TO FUNCTION PROPERLY. THE REGISTERS VERIFIED ARE:

WORD COUNT <16 BITS WIDE>  
BUFFER ADDRESS <15 BITS WIDE>  
DATA BUFFER <16 BITS WIDE>  
STATUS REGISTER <6 BITS TESTED>

8. OPERATION OPTIONS  
-----

NONE

9. NON STANDARD PRINTOUTS  
-----

ALL PRINTOUTS HAVE STANDARD MEANINGS AS REPRESENTED IN  
DEC/X11 DOCUMENTATION.

10. ENVIROMENT  
-----

#1 LSI-11 WITH 16K OF MEMORY  
RX11 FLOPPY DISK CONTROLLER WITH 2 DRIVE  
DRV11B INTERPROCESSOR INTERFACE WITH WRAPAROUND CABLE

#2 LSI-11 WITH 24K OF MEMORY (8K CORE + 16K MOS)  
RX11 FLOPPY DISK CONTROLLER  
DRV11B INTERPROCESSOR BUFFER  
DRV11B INTERPROCESSOR BUFFER  
DRV11B INTERPROCESSOR BUFFER

```
134 000000- IOMDX <DRFB > 172410,124,4,0,0,1000,121,BUFFER,512.
135 000000- MODULE 150000,DRFB,172410,124,4,0,0,1000,121,BUFFER,512.,
137 ; TITLE DRFB DEC/X11 SYSTEM EXERCISER MODULE
138 ; DIXCDM VERSION 6 23-MAY-78
139 ;LIST
140 *****
141 000000- RECTN: .ASCII /DRFB / ;MODULE NAME
142 000000- MODNAM: .ASCII /DRFB / ;MODULE NAME
143 000005- 0000 0000 ;USED TO KEEP TRACK OF WBUFF USAGE
144 000000- 172410 ADDR: 172410+0 ;1ST DEVICE ADDR
145 000012- 0000 VECTOR: 124+0 ;1ST DEVICE VECTOR.
146 000013- 0000 BR1: .BYTE PRTV4+0 ;1ST BR LEVEL.
147 000013- 0000 BR2: .BYTE PRTV0+0 ;2ND BR LEVEL.
148 000016- 000000 DVID1: 0+1 ;DEVICE INDICATOR 1.
149 000020- 000000 SR1: OPEN ;SWITCH REGISTER 1
150 000024- 000000 SR2: OPEN ;SWITCH REGISTER 2
151 000024- 000000 SR3: OPEN ;SWITCH REGISTER 3
152 000024- 000000 SR4: OPEN ;SWITCH REGISTER 4
153 *****
154 000026- 150000 STAT: 150000 ;STATUS WORD.
155 000030- 000264 INIT: START ;MODULE START ADDR.
156 000034- 000250 SPOINT: MODSP ;MODULE STACK POINTER.
157 000036- 001000 PASCNT: 0 ;PASS COUNTER.
158 000040- 000000 ICOUNT: 0 ;# OF ITERATIONS PER PASS=1000
159 000044- 000000 SOFCNT: 0 ;LOC TO COUNT ITERATIONS
160 000044- 000000 SOFPCNT: 0 ;LOC TO SAVE TOTAL SOFT ERRORS
161 000046- 000000 SOFPCNT: 0 ;LOC TO SAVE TOTAL HARD ERRORS
162 000050- 000000 SOFPCNT: 0 ;LOC TO SAVE SOFT ERRORS PER PASS
163 000054- 000000 HRDPAS: 0 ;LOC TO SAVE HARD ERRORS PER PASS
164 000054- 000000 SYSCNT: 0 ;# OF SYS ERRORS ACCUMULATED
165 000056- 000000 RANUM: 0 ;HOLDS RANDOM # WHEN RAND MACRO IS CALLED
166 000056- 000000 COMPIG: 0 ;RESERVED FOR MONITOR USE
167 000056- 000000 RES1: 0 ;RESERVED FOR MONITOR USE
168 000064- 000000 SVR0: OPEN ;LOC TO SAVE R0.
169 000064- 000000 SVR1: OPEN ;LOC TO SAVE R1.
170 000066- 000000 SVR2: OPEN ;LOC TO SAVE R2.
171 000070- 000000 SVR3: OPEN ;LOC TO SAVE R3.
172 000074- 000000 SVR4: OPEN ;LOC TO SAVE R4.
173 000074- 000000 SVR5: OPEN ;LOC TO SAVE R5.
174 000076- 000000 SVR6: OPEN ;LOC TO SAVE R6.
175 000100- 000000 CSRA: OPEN ;ADDR OF CURRENT CSR.
176 000102- 000000 SBADR: OPEN ;ADDR OF GOOD DATA, OR
177 000104- 000000 WABADR: OPEN ;ADDR OF BAD DATA, OR
178 000104- 000000 ASTAT: OPEN ;STATUS REG CONTENTS.
179 000106- 000000 ERRTYP: 0 ;TYPE OF ERROR
180 000106- 000000 ASB: OPEN ;EXPECTED DATA.
181 000110- 000000 RSTRT: RESTR ;ACTUAL DATA
182 000110- 000000 WDFD: OPEN ;RESTART ADDRESS AFTER END OF PASS
183 000114- 000264 WDFR: OPEN ;WORDS FROM MEMORY PER ITERATION
184 000116- 000000 INTR: OPEN ;# OF INTERRUPTS PER ITERATION
185 000120- 000000 IDNUM: 121 ;MODUL IDENTIFICATION NUMBER=121
186 000124- 000121 RBUFVA: BUFFER ;READ BUFFER VIRTUAL ADDRESS
187 000126- 000000 RBUFFA: OPEN ;READ BUFFER PHYSICAL ADDRESS
```

```
190 000130- 000000 RBUFEA: OPEN ;READ BUFFER EA BITS
191 000132- 001000 RBUFSZ: 512 ;SIZE OF THE READ BUFFER
192 000134- 000000 WBUFEA: OPEN ;WRITE BUFFER PHYSICAL ADDRESS
193 000136- 000000 WBUFSZ: OPEN ;WRITE BUFFER EA BITS
194 000140- 000000 WBUFRO: OPEN ;WRITE BUFFER SIZE REQUESTED
195 000142- 000000 WBUFSA: OPEN ;WRITE BUFFER SIZE AVAILABLE
196 000144- 000000 CDWCT: OPEN ;C/DATA/DATCK ERROR COUNT
197 000146- 000000 FREE: OPEN ;C/DATA/DATCK WORD COUNT
198 000146- 000000 FREE: OPEN ;RESERVED FOR FUTURE USE
199 000146- 000040 FREE: .REPT ;MODULE STACK STARTS HERE.
200 .NLIST
201 .WORD 0
202 .LIST
203 .ENDR
204 000250- MODSP:
205 *****
```

```

206 ;DRV11B BUS REGISTER ADDRESS POINTERS
207
208 000250 172410 DRVWCR: 172410 ;WORD COUNT
209 000252 172412 DRVBAR: 172412 ;BUFFER ADDRESS
210 000254 172414 DRVCSR: 172414 ;COMMAND/STATUS
211 000256 172416 DRVDBR: 172416 ;DATA BUFFER
212
213 ;DRV11B VECTOR ADDRESS POINTERS
214
215 000260 000124 DRVCT0: 124 ;READY, NEX & INCOMPLETE DATIO VECTOR
216 000262 000126 DRVCT2: 126 ;NEW PSW ON INTR
217
218 ;COMMON PROGRAM LOCATION(S)
219
220 TPS=177564
221
222 000264
223 000264 012767 000232 177622 RESTR: MOV #232, WDT0 ;232 WORDS TO MEM/ITERATION
224 000264 012767 000232 177616 START: MOV #232, WDFR ;232 WORDS FROM MEM/ITERATION
225 000264 012767 000000 177612 MOV #6, INTR ;6 INTERRUPTS/ITERATION
226 000306 012767 000000 177470 MOV #DRVWCR, RO ;SET UP REG ADRS POINTER
227 000312 012767 000000 177470 MOV #DRVBAR, RO ;SET UP REG ADRS POINTER
228 000316 010120 000000 000002 SETUP2: MOV ADDR, R1 ;GET BASE ADRS
229 000324 092700 000260 000260 ADD #2, R1 ;LOAD EM
230 000330 001372 000000 000000 CMP #DRVDBR+2, RO ;ALL DONE?
231 000332 012700 000260 000260 BNE SETUP3 ;BR IF NOT
232 000336 012700 177446 177446 MOV #DRVCT0, RO ;SET DRV11B VECTOR ADRS POINTER
233 000344 062761 000002 000002 SETUP3: MOV VECTOR, R1 ;GET BASE VECTOR ADRS
234 000350 022700 000264 000264 ADD #2, R1 ;POINT TO NEXT
235 000354 012700 000000 000000 CMP #DRVCT2+2, RO ;ALL DONE?
236 000358 012700 177674 177674 BNE SETUP3 ;BR IF NOT
237 000364 005077 177672 177672 MOV #DRVCT2, @DRVCT0
238 000370 104415 000000 000124 CLR @DRVCT2
239
240 GETPA$, BEGIN, RBUFVA ;GET PHYSICAL ADDRESS FROM 16-BIT RBUFVA

```

```

243 ;TEST THAT THE WORD COUNT REG IS WRITE/READABLE (FLOAT 0 COM PTRN)
244
245 000376 016767 177646 177474 TSTWC: MOV DRVWCR, CSRA ;SET UP WC REG ADRS
246 000404 005000 000000 000000 CLR RO ;RO SAYS SHIFT PTRN WHEN 0
247 000408 012767 177776 177470 MOV #2, ASTAT ;FLOAT 0 RIGHT TO LEFT
248 000414 016777 177454 177626 1S: MOV #2, ASTAT, @DRVWCR ;LD WC
249 000422 012767 177622 177452 MOV @DRVWCR, ACSR ;READ IT BACK
250 000430 026767 177450 177444 CMP ASTAT, ACSR ;COMPARE RESULTS
251 000438 001406 000002 177440 BEQ #25, ERR1YP ;BR IF SO
252 000440 012767 000002 177440 MOV #25, ERR1YP ;BIT STUCK
253
254 000446 104405 000000 000000 ;*****
255 HRDRS, BEGIN, NULL ;WORD COUNT WRITE/READ FAILURE
256 ;*****
257 000454 005167 177424 2S: COM ASTAT ;COMPLEMENT ZERO
258 000460 005100 000000 000000 COM RO ;RO SAYS SHIFT LEFT WHEN = 0
259 000466 001354 000000 000000 BNE 1S ;TRY THE COMPLEMENT IF RO NOT 0
260 000468 006367 177414 177410 ASL ASTAT ;COMPLEMENT WAS DONE - NOW SHIFT ZERO LEFT
261 000474 103747 177410 177410 INC ASTAT ;KEEP LSB SET
262 BCS 1S ;AGAIN TILL ALL PATRNS DONE
263
264 ;TEST THAT THE BUFFER ADDRESS REG IS WRITE/READABLE (FLOAT 0 COM PTRN)
265
266 000476 016767 177550 177374 TSTBA: MOV DRVBAR, CSRA ;SET UP BA REG ADRS
267 000504 005000 000000 000000 CLR RO ;RO SAYS SHIFT PTRN WHEN 0
268 000512 012767 177774 177370 MOV #4, ASTAT ;FLOAT 0 RIGHT TO LEFT
269 000520 012767 177364 177530 1S: MOV #4, ASTAT, @DRVBAR ;LD BA
270 000530 042767 000001 177324 MOV @DRVBAR, ACSR ;READ IT BACK
271 000536 026767 177342 177336 BIC #BIT0, ACSR ;DON'T WANT BIT0
272 000544 001406 000002 177332 CMP ASTAT, ACSR ;COMPARE RESULTS
273 000546 012767 000025 177332 BEQ #25, ERR1YP ;BR IF SO
274 000554 104405 000000 000000 MOV #25, ERR1YP ;BIT STUCK
275
276 000562 005167 177316 2S: HRDRS, BEGIN, NULL ;BUS ADRS WRITE/READ FAILURE
277 ;*****
278 000566 042767 000001 177310 COM ASTAT ;COMPLEMENT ZERO
279 000574 005100 000000 000000 BIC #BIT0, ASTAT ;BIT 00 NOT INVOLVED
280 000576 001354 000000 000000 COM RO ;RO SAYS SHIFT LEFT WHEN = 0
281 000600 006367 177300 177300 BNE 1S ;TRY THE COMPLEMENT IF RO NOT 0
282 000604 103004 000002 177270 ASL ASTAT ;COMPLEMENT WAS DONE - NOW SHIFT ZERO LEFT
283 000606 062767 000002 177270 BCC TSTD0 ;KEEP ADDR LSB SET
284 000614 00737 000000 000000 ADD #2, ASTAT ;NEXT TEST OF BIT 15 DONE
285 BR 1S ;AGAIN TILL ALL PATTERNS DONE

```

```

286
287
288 000616 016767 177434 177254 ;TEST THAT THE DATA BUFFER REG IS WRITE/READABLE (FLOAT 0 COM PTRN)
289 000624 005000 TSTDB: MOV DRVDBR,CSRA ;SET UP DB REG ADRS
290 000626 012767 177776 177250 CLR RO ;RO SAYS SHIFT PTRN WHEN 0
291 000634 016777 177244 177414 1S: MOV #2,ASTAT ;FLOAT 0 RIGHT TO LEFT
292 000642 017767 177410 177332 MOV ASTAT,DRVDBR ;LD DB
293 000650 026767 177230 177224 MOV DRVDBR,ACSR ;READ IT BACK
294 000656 001406 CMP ASTAT,ACSR ;COMPARE RESULTS
295 000660 012767 000025 177220 BEQ #25,ERRTYP ;BR IF SO
296 ;*****
297 000666 104405 000000 000000 ;*****
298 HRDERS,BEGIN,NULL ;DATA BUFFER WRITE/READ FAILURE (LOOP BACK)
299 000674 005167 177204 2S: COM ASTAT ;COMPLEMENT ZERO
300 000700 001358 BNE RO ;RO SAYS SHIFT LEFT WHEN = 0
301 000702 001358 MOV RO,ASTAT ;TRY THE COMPLEMENT IF RO NOT 0
302 000704 006367 177174 ASL #1,ASTAT ;COMPLEMENT WAS DONE - NOW SHIFT ZERO LEFT
303 000710 005267 177170 INC ASTAT ;KEEP LSB SET
304 000714 103747 BCS #1 ;AGAIN TILL ALL PATRNS DONE
305
306 ;TEST THAT THE DATA BUFFER REG IS BYTE ADDRESSABLE
307 TSTDB: MOV DRVDBR,RO ;GET DB REG ADRS
308 MOV RO,CSRA ;SET UP DB REG ADRS
309 CLR #0 ;ZERO DATA BUFFER REG
310 MOV #177177,ASTAT ;LD EXPECTED
311 MOV #177776,ACSR ;SEND DATA FROM "BDDAT"
312 BLSB ACSR,1(RO) ;LOAD HI BYTE DB
313 BLSB (RO),ACSR ;LOAD LO BYTE DB
314 MOV ASTAT,ACSR ;READ BACK
315 CMP ASTAT,ACSR ;COMPARE RESULTS
316 BEQ #25,ERRTYP ;NEXT TEST IF SO
317 ;*****
318 001000 104405 000000 000000 ;*****
319 HRDERS,BEGIN,NULL ;DATA ERROR ON BYTE ADDRESSING THE DATA BUFFER REG
320 ;*****
321
322

```

```

323
324
325 001006 016767 177242 177064 ;TEST THAT THE 3 "FNCT" BITS CONTROL THE 3 "STAT" BITS (COUNT PTRN)
326 001014 012767 007000 177062 TSTFN3: MOV DRVCSR,CSRA ;SET UP CSR ADRS
327 001022 012700 000016 MOV #7000,ASTAT ;LD EXPECTED
328 001026 010077 177222 1S: MOV #16,RO ;RO CONTAINS "FNCT" BITS WRITTEN
329 001032 017767 177216 177042 MOV RO,DRVCSR ;WRITE INTO "FNCT" BITS
330 001040 042767 170777 177034 MOV DRVCSR,ACSR ;READ BACK THRU "STAT" BITS
331 001046 026767 177032 177026 BIC #170777,ACSR ;MASK TO "STAT" BITS ONLY
332 001054 001406 CMP ASTAT,ACSR ;COMPARE RESULTS
333 001056 012767 000025 177022 BEQ #25,ERRTYP ;BR IF SO
334 ;*****
335 001064 104405 000000 000000 ;*****
336 HRDERS,BEGIN,NULL ;"FNCT" BITS FAILED TO SET "STAT" BITS (LOOP BACK)
337 001072 162767 001000 177004 2S: SUB #1000,ASTAT ;CHANGE TO NEXT EXPECTED
338 001100 162700 000002 SUB #2,RO ;DECREASE COUNT PATTERN
339 001104 100350 BPL #1 ;DO AGAIN UNTIL 0 TESTED
340
341 ;TEST THAT GO CLR8 READY & FNCT 2 WILL SET IT
342 TSTFAC: MOV DRVCSR,CSRA ;SET UP CSR ADRS
343 CLR ASTAT ;EXPECT 0
344 MOV #4,DRVCSR ;SET GO WHICH SHOULD CLR READY
345 MOV DRVCSR,ACSR ;READ THE CSR
346 CMP ASTAT,ACSR ;COMPARE RESULTS
347 001142 001406 BEQ #15,ERRTYP ;BR IF SO
348 001144 012767 000025 176734 ;*****
349 001152 104405 000000 000000 ;*****
350 HRDERS,BEGIN,NULL ;THE GO BIT FAILED TO CLR READY
351 001160 052777 000004 177066 1S: MOV #2204,ASTAT ;FNCT 2 SHOULD SET READY
352 001166 012767 002204 176710 MOV #4,DRVCSR ;LD EXPECTED
353 001174 017767 177123 176746 MOV DRVCSR,ACSR ;GET CSR
354 001202 026767 176676 176672 CMP ASTAT,ACSR ;COMPARE RESULTS
355 001210 001406 BEQ TSTBAC ;NEXT TEST IF SET
356 001212 012767 000025 176666 MOV #25,ERRTYP ;BIT STUCK
357 ;*****
358 001220 104405 000000 000000 ;*****
359 HRDERS,BEGIN,NULL ;FNCT 2 (VIA ATTN) FAILED TO SET READY
360 ;*****
361
362

```

```

363          ;TEST THAT READY CONTROLS "BAR" BIT00
364
365 001226* 012777 000004 177020 TSTBAR: MOV #4, @DRVCSR ;SET READY
366 001234* 016767 177012 176636 MOV @RVBAR, CSRA ;SET UP BAR ADRS
367 001234* 012767 000001 176634 MOV #1, ASTAT ;EXPECT LSB OF BAR
368 001250* 005077 176776 CLR @RVBAR, ACSR ;CLR BAR
369 001254* 017767 176670 176620 MOV @RVBAR, ACSR ;READ BAR
370 001263* 026767 176616 176612 CMP ASTAT, ACSR ;COMPARE RESULTS
371 001270* 001406 000025 176606 BEQ 15 ;BR IF SO
372 001272* 012767 000025 176606 MOV #25, @ERRTYP ;BIT STUCK
373
374 001300* 104405 000000* 000000 HRDERS, BEGIN, NULL ;AOO FAILED TO READ A ONE (SR TIED TO RDV)
375
376 001306* 012777 000001 176740 1$: MOV #1, @DRVCSR ;SET GO (CLRS BAR BIT00)
377 001314* 017767 176732 176560 MOV @RVBAR, ACSR ;READ BAR
378 001322* 001410 176554 176550 BEQ 25 ;BR IF ZERO
379 001330* 005067 000025 176550 CLR ASTAT, ACSR ;EXPECTED ZERO
380 001330* 012767 000025 176550 MOV #25, @ERRTYP ;BIT STUCK
381
382 001336* 104405 000000* 000000 HRDERS, BEGIN, NULL ;WHEN RDY CLRD - AOO FAILED TO READ A ZERO
383 001344* 012777 000004 176702 2$: MOV #4, @DRVCSR ;INSURE RDY SET BEFORE ADVANCING
384
385 ;TEST THAT "CYCLE" WILL CLOCK THE DBR (IN)
386
387 TSTCLK: MOV @DRVDBR, CSRA ;SET UP DBR ADRS
388 001360* 012767 125252 176526 MOV #125252, ASTAT ;LD EXPECTED
389 001366* 016777 176512 176662 MOV #1, @RVDBR ;LD DBR WITH #125252
390 001374* 012777 000400 176652 MOV #400, @DRVCSR ;SET CYCLE - SHOULD CLK DBR (IN)
391 001382* 017767 176642 176446 MOV #52525, @RVDBR ;CHANGE DBR (OUT) DATA - SHOULD NOT AFFECT (IN)
392 001390* 017767 176642 176446 MOV @RVDBR, ACSR ;READ DBR
393 001410* 017767 176642 176446 CMP ASTAT, ACSR ;COMPARE RESULTS
394 001416* 026767 176462 176456 BEQ 15 ;BR IF SO
395 001424* 001406 000025 176452 MOV #25, @ERRTYP ;BIT STUCK
396 001426* 012767 000025 176452
397
398 001434* 104405 000000* 000000 HRDERS, BEGIN, NULL ;CYCLE DID NOT ATCH DBR (IN) DATA
399
400 001442* 042777 000400 176604 1$: BIC #400, @DRVCSR ;REMOVE CYCLE
401 001450* 012767 052525 176426 MOV @RVDBR, ASTAT ;NOW EXPECT #52525
402 001456* 017767 176414 176416 MOV @RVDBR, ACSR ;READ DBR
403 001464* 026767 176414 176410 CMP ASTAT, ACSR ;COMPARE RESULTS
404 001474* 012767 000025 176404 BEQ 15 ;NEXT TEST IF SO
405 001474* 012767 000025 176404 MOV #25, @ERRTYP ;BIT STUCK
406
407 001502* 104405 000000* 000000 HRDERS, BEGIN, NULL ;DBR FAILED TO READ WHEN CYCLE CLRD (NORMAL)
408
409

```

```

410          ;TEST SINGLE "DATI" NPR TRANSFERS (FLOATING 0 COMPLEMENT PATRN)
411
412 TSTDIT: JSR R5, SETVEC ;GO SET UP INTERRUPT RETURN
413 001510* 004567 001410 2$: ;RETURN TO 2$ ON INTR
414 001514* 001564 177776 CLR #2, R0 ;FLOAT ZERO RIGHT TO LEFT
415 001522* 005001 177777 176516 1$: MOV #1, @RVNCR ;DO ONE XFER
416 001532* 012777 176370 176512 MOV #1, @RVNCR ;DO ONE XFER
417 001540* 010067 002074 176502 MOV @RVNCR, @RVBAR ;GET DATA WORD FROM "DBUF"
418 001544* 012777 000101 176502 MOV #1, @RVNCR ;SET UP MEM DATA
419 001550* 104400 000400 176474 BIS #400, @DRVCSR ;SET CYCLE
420 001564* 104400 000000* 000000 EXITS, BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
421
422
423 001564* 000004 000000* 001572* 5$: ;IRQS, BEGIN, 5$ ; QUEUE UP TO CONTINUE AT 5$ AND RTI
424
425 JSR R5, CKSTAT ;GO CHECK STATUS
426 001572* 004567 001362 1 MOV #00, @RVNCR ;CSR STATUS EXPECTED
427 001576* 000700 000032 176276 MOV #32, @ERRTYP ;NPR ERROR
428 001600* 000001 000032 176276 HRDERS, BEGIN, NULL ;RETURN HERE IF STATUS ER - EXPECTED CYCLE, READY & IR
429 001610* 104405 000000* 000000 BR 45 ;NPR ERROR
430 001616* 000451 000032 176260 HRDERS, BEGIN, NULL ;RETURN HERE IF WC ER - EXPECTED 0
431 001620* 012767 000032 176260 MOV #45, @ERRTYP ;NPR ERROR
432 001626* 104405 000000* 000000 HRDERS, BEGIN, NULL ;RETURN HERE IF WC ER - EXPECTED 0
433 001634* 000442 000032 176242 BR 45 ;NPR ERROR
434 001636* 012767 000032 176242 MOV #45, @ERRTYP ;NPR ERROR
435 001644* 104405 000000* 000000 HRDERS, BEGIN, NULL ;RETURN HERE IF BAR ER - SHOULD = DBUF+2
436 001652* 000433 003640 176220 BR 45 ;RETURN HERE IF OK - SET UP XFER ADRS
437 001654* 012767 003640 176214 MOV #BUFFER, SBADR ;GO RESTORE VECTOR
438 001662* 012767 176212 176214 MOV #BUFFER, WASADR ;RETURN HERE IF OK - SET UP XFER ADRS
439 001670* 010067 176356 176206 MOV #0, CASB ;LD EXPECTED
440 001674* 017767 176176 176172 MOV @RVDBR, ANAS ;READ DATA XFERED
441 001700* 001406 176176 176172 CMP ASTAT, ACSR ;COMPARE RESULTS
442 001712* 016767 176340 176160 BEQ 35 ;BR IF SO
443 001720* 104404 000000* 000000 MOV @RVDBR, CSRA ;SET UP DBR ADRS
444
445 DATERS, BEGIN ;DATA ERROR!!!!
446
447 BR 45 ;RETURN HERE ON GOOD DATA - NOW COM PATRN
448 001724* 000406 005100 3$: COM R0 ;KEEP TRACK OF COMPLEMENT
449 001726* 005100 001274 176206 COM R1 ;DO COMPLEMENT OF THIS FLOATING ZERO IF 0
450 001734* 006300 005200 176172 INC R0 ;WAS DONE - NOW SHIFT ZERO LEFT
451 001736* 005200 103677 176160 BCS 15 ;KEEP LSB SET
452 001740* 004767 001172 4$: JSR PC, RSTVEC ;GO RESTORE VECTOR
453
454

```

```

463 ;TEST SINGLE "DATO" NPR TRANSFERS (FLOATING 0 COMPLEMENT PATRN)
465 TSTDTO: JSR R5,SETVEC ;GO SET UP INTERRUPT RETURN
466 ;RETURN TO 2S ON INTR
467 MOV #2,R0 ;LOAD ZERO RIGHT TO LEFT
468 CLR R1 ;R1 CONTROLS DATA SHIFTING
469 MOV #1,DRVWCR ;DO ONE XFER
470 MOV #0,DRVBAR ;WRITE DATA WORD TO "DBUF"
471 MOV #0,DRVBR ;SET UP DATA IN DBR
472 MOV #103,DRVCSR ;SET IE GO & FNCT 1 (CONTROL)
473 BIS #100,DRVCSR ;SET CYCLE
474 EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
475
476 2S:
477 PIRQS,BEGIN,5$ ;-----
478 ; QUEUE UP TO CONTINUE AT 5$ AND RTI
479
480 5S: JSR R5,CKSTAT ;GO CHECK STATUS
481 ;CSR STATUS EXPECTED
482 MOV #32,ERRTYP ;# OF XFERS
483 ;NPR ERROR
484 ;*****
485 HRDERS,BEGIN,NULL ;RETURN HERE IF STATUS ER - EXPECTED STAT C
486 ;*****
487 BR 4$ ;CYCLE, READY, IE & FNCT 1
488 MOV #32,ERRTYP ;GO RESTORE VECTOR
489 ;NPR ERROR
490 ;*****
491 HRDERS,BEGIN,NULL ;RETURN HERE IF WC ER - EXPECTED 0
492 ;*****
493 BR 4$ ;GO RESTORE VECTOR
494 MOV #32,ERRTYP ;NPR ERROR
495 ;*****
496 HRDERS,BEGIN,NULL ;RETURN HERE IF BAR ER - SHOULD = DRUF+2
497 ;*****
498 BR 4$ ;GO RESTORE VECTOR
499 MOV #BUFFER,SBADR ;RETURN HERE IF OK - SET UP XFER ADRS
500 ;#BUFFER,WASADR
501 MOV #0,ASB ;LD EXPECTED
502 MOV #BUFFER,AMAS ;GET DATA XFERED
503 CMP #STAT,ACSR ;COMPARE RESULTS
504 BEQ 2S ;IF SO
505 MOV #DRVDBR,CSRA ;SET UP DBR ADRS
506 ;*****
507 DATERS,BEGIN ;DATA ERROR!!!
508 ;*****
509 BR 4$ ;GO RESTORE VECTOR
510 COM R1 ;RETURN HERE ON GOOD DATA - NOW COM PATRN
511 BNE 1$ ;KEEP TRACK OF COMPLEMENT
512 ASL R0 ;DO COMPLEMENT OF THIS FLOATING ZERO IF 0
513 LMC R0 ;COMPLEMENT WAS DONE - NOW SHIFT ZERO LEFT
514 ;KEEP LSB SET
515 INR R0 ;AGAIN TILL ZERO BIT IN CARRY
516 JSR PC,RSTVEC ;GO RESTORE VECTOR

```

```

517 ;TEST FOR 15 "DATI" NPR TRANSFERS (BURST MODE)
518
519 1200: JSR R5,SETVEC ;GO SET UP INTERRUPT RETURN
520 ;RETURN TO 1S ON INTR
521 MOV #15,DRVWCR ;GO LOAD BUFFER WITH COMPLEMENTING PATRN
522 MOV #0,DRVBAR ;LOAD WC REG - WILL DO 15 XFERS
523 MOV #101,DRVCSR ;SET UP CURRENT ADRS
524 BIS #100,DRVCSR ;SET IE GO
525 EXITS,BEGIN ;SET CYCLE
526 ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
527
528 1S:
529
530 3S: PIRQS,BEGIN,3$ ;-----
531 ; QUEUE UP TO CONTINUE AT 3$ AND RTI
532
533 JSR R5,CKSTAT ;GO CHECK STATUS
534 ;CSR STATUS EXPECTED
535 MOV #32,ERRTYP ;# OF XFERS
536 ;NPR ERROR
537 ;*****
538 HRDERS,BEGIN,NULL ;RETURN HERE IF STATUS ER - EXPECTED CYCLE, READY & IE
539 ;*****
540 BR 2S ;CYCLE, READY, IE & FNCT 1
541 MOV #32,ERRTYP ;GO RESTORE VECTOR
542 ;NPR ERROR
543 ;*****
544 HRDERS,BEGIN,NULL ;RETURN HERE IF WC ER - EXPECTED 0
545 ;*****
546 BR 2S ;GO RESTORE VECTOR
547 MOV #32,ERRTYP ;NPR ERROR
548 ;*****
549 HRDERS,BEGIN,NULL ;RETURN HERE IF BAR ER - SHOULD = DRUF+6
550 ;*****
551 BR 2S ;GO RESTORE VECTOR
552 MOV #177577,ASB ;OK - SET UP LAST XFER ADRS WHERE #177577 SHOULD BE
553 MOV #DRVDBR,AMAS ;LD EXPECTED
554 CMP #STAT,ACSR ;DBR SHOULD HAVE LAST DATUM
555 BEQ 2S ;COMPARE RESULTS
556 MOV #DRVDBR,CSRA ;BR IF SO
557 ;*****
558 DATERS,BEGIN ;DATA ERROR!!!
559 ;*****
560 JSR PC,RSTVEC ;GO RESTORE VECTOR

```



```
559 ;TEST FOR 15 "DATO" NPR TRANSFERS (BURST MODE)
560 0200: JSR R5,SETVEC ;GO SET UP INTERRUPT RETURN
561 IS ;RETURN TO IS ON INTR
562 MOV #15,@DRVWCR ;WORD WC REG - WILL DO 15 XFER'S
563 MOV R00PPA,@DRVBAR ;SET UP CURRENT ADRS
564 MOV #17737,@DRVDDBR ;THIS WILL BE WRITTEN TO MEM
565 MOV #109,@DRVCSR ;SET IE, FNCT 1 & GO
566 BIS #400,@DRVCSR ;SET CYCLE
567 EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
568
569 1$:
570 ;
571 PIRQS,BEGIN,3$ ; QUEUE UP TO CONTINUE AT 3$ AND RTI
572 ;
573 3$: JSR R5,CKSTAT ;GO CHECK STATUS
574 ;EXPECTED STATUS
575 IS ;# OF XFERS
576 MOV #32,ERRTYP ;NPR ERROR
577 ;*****
578 HRDERS,BEGIN,NULL ;RETURN HERE IF STATUS ER - EXPECTED STAT C
579 ;*****
580 BR 2$ ;CYCLE, READY IE & FNCT 1
581 ;GO RESTORE VECTOR
582 MOV #32,ERRTYP ;NPR ERROR
583 ;*****
584 HRDERS,BEGIN,NULL ;RETURN HERE IF WC ER - EXPECTED 0
585 ;*****
586 MOV #32,ERRTYP ;GO RESTORE VECTOR
587 ;NPR ERROR
588 ;*****
589 HRDERS,BEGIN,NULL ;RETURN HERE IF BAR ER - SHOULD = DBUF+6
590 ;*****
591 BR 2$ ;GO RESTORE VECTOR
592 JSR PC,CKDAT ;RETURN HERE IF OK - NOW GO CHECK DATA
593 ;*****
594 DATERS,BEGIN ;DATA ERROR!!!
595 ;*****
596 2$: JSR PC,RSTVEC ;RETURN HERE IF DATA CHECK OK - GO RESTORE VECTOR
597
```

```
598 ;TEST FOR 200 NPR TRANSFERS IN MAINT MODE
599 MNTIN: JSR R5,SETVEC ;GO SET UP INTR RETURN
600 ;RETURN TO 2$ ON INTR
601 JSR PC,LDDBUF1 ;GO SET UP DBUF (SPECIAL COM PATTERN)
602 MOV R00PPA,@DRVBAR ;SET UP CURRENT ADRS
603 MOV #200,@DRVWCR ;SET UP FOR 200 XFER'S
604 MOV #10101,@DRVCSR ;SET WAIT, IE & GO
605 BIS #400,@DRVCSR ;SET CYCLE, IE & GO
606 MOV #0,@MAS ;SET UP A COUNTER
607 EXITS,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
608
609 2$:
610 ;
611 PIRQS,BEGIN,4$ ; QUEUE UP TO CONTINUE AT 4$ AND RTI
612 ;
613 4$: JSR R5,CKSTAT ;GO CHECK STATUS
614 ;EXPECTED STATUS
615 200- ;# OF XFERS
616 MOV #32,ERRTYP ;NPR ERROR
617 ;*****
618 HRDERS,BEGIN,NULL ;RETURN HERE IF STATUS ER - EXPECTED MAINT CYCLE, READY
619 ;*****
620 BR 3$ ;GO RESTORE VECTOR
621 ;GO RESTORE VECTOR
622 MOV #32,ERRTYP ;NPR ERROR
623 ;*****
624 HRDERS,BEGIN,NULL ;RETURN HERE IF WC ER - SHOULD BE 0
625 ;*****
626 BR 3$ ;GO RESTORE VECTOR
627 ;NPR ERROR
628 MOV #32,ERRTYP ;GO RESTORE VECTOR
629 ;NPR ERROR
630 ;*****
631 HRDERS,BEGIN,NULL ;RETURN HERE IF BAR ER - SHOULD = BUFFER+40
632 ;*****
633 BR 3$ ;GO RESTORE VECTOR
634 JSR R5,CKDAT1 ;RETURN HERE IF OK - NOW GO CHECK DATA
635 ;*****
636 DATERS,BEGIN ;DATA ERROR!!!
637 ;*****
638 3$: NOP ;RESTORE VECTOR NEXT
639 JSR PC,RSTVEC ;GO RESTORE VECTOR
```

```
639 )TEST THE ADDRESS (I/O) ABILITY TO THE TTY PRINTER CSR
640 TSTTTY: JSR R5,SETVEC ;GO SET UP INTR RETURN
641 1S ;RETURN TO 1S ON INTR
642 MOV #1,@DRVWCR ;SET UP WC - 1 XFER
643 MOV #1PS,@DRVBAR ;SET UP BUFFER ADRS - FROM PRINTER CSR
644 CLR @DRVDBR ;ZERO THE DBR
645 MOV #161,@DRVCSR ;SET IE, XAD17 & XAD16, & GO
646 BIC #1400,@DRVCSR ;SET CYCLE
647 EXIT$,BEGIN ;EXIT TO MONITOR. MODULE WAIT FOR INTERRUPT.
648
649
650 1S:
651 003020* 000004 000000* 003026*
652 ;-----
653 ;IRQS,BEGIN,5S ; QUEUE UP TO CONTINUE AT 5S AND RTI
654 ;-----
655 5S: JSR R5,CKSTAT ;GO CK STATUS
656 760 ;CSR EXPECTED STATUS
657 1 ;# OF XFER'S
658 MOV #32,ERRTYP ;NPR ERROR
659 ;*****
660 @RDERS,BEGIN,NULL ;RETURN HERE IF STATUS ER - EXPECTED CYCLE
661 ;*****
662 BR 2S ;XAD17 & XAD16, RDV & IE
663 MOV #32,ERRTYP ;GO RESTORE VECTOR
664 ;*****
665 003062* 104405 000000* 000000
666 @RDERS,BEGIN,NULL ;RETURN HERE IF WC ER - EXPECTED 0
667 ;*****
668 NOP ;MUST LEAVE THESE NOP'S IN
669 NOP
670 2S: JSR PC,RSTVEC ;GO RESTORE VECTOR
671 MOV #4,@DRVCSR ;GO RESTORE VECTOR
672 CLR @DRVCSR ;CLEAR DEVICE
673 ;END OF PASS
674
675 ENDS$,BEGIN ;SIGNAL END OF ITERATION.
676 JMP TSTWC ;MONITOR SHALL TEST END OF PASS
677 ;TEST THE DEVICE AGAIN
```

```
678 ;THIS ROUTINE SETS THE PRIORITY LEVEL FOR NO INTERRUPT -
679 ;SETS UP THE DRV11B INTERRUPT TO RETURN ON INTERRUPT
680 ;TO THE ADDRESS INDICATED ((R5)) BY THE CALL #2
681 SETVEC: MOV (R5),@DRVCT0 ;SET UP INTR RETURN ADRS
682 MOV #200,@DRVCT2 ;REP PRIORITY LEVEL AT TOP ON INTR
683 RTS R5 ;EXIT
684
685 ;THIS ROUTINE CLEARS THE DRV11B CSR - RESTORES THE DRV11R
686 ;INTERRUPT VECTOR TO A HALT - RAISES PRIORITY LEVEL
687 RSTVEC: CLR @DRVCSR ;CLR STATUS & CONTROL
688 MOV @DRVCT2,@DRVCT0 ;POINT VECTOR TO HALT
689 CLR @DRVCT2 ;SET UP HALT
690 RTS PC ;EXIT
691
692 ;THIS ROUTINE CHECKS ON ALL DATA TRANSFERS FOR: CORRECT STATUS,
693 ;CORRECT WORD COUNT & CORRECT BUFFER ADDRESS - THE EXPECTED DATA IS
694 ;SUPPLIED IN THE CALL #2 & #4 - THE RETURN IS TO #54 IF NO ERRORS
695 ;DETECTED - IF AN ERROR IS DETECTED THE RETURN IS TO THE APPROPRIATE ERROR EMT
696 CKSTAT: MOV @DRVCSR,ACSR ;READ THE STATUS
697 BIC #100,@DRVCSR ;DISABLE THE IE BIT
698 MOV (R5),ASTAT ;SET UP EXPECTED STATUS
699 CMP ASTAT,ACSR ;COMPARE
700 BEQ 1S ;IF SO
701 MOV @DRVCSR,CSRA ;SET UP CSR ADRS
702 ADD #2,R5 ;POINT TO THE CSR ER
703 RTS R5 ;EXIT HERE ON STATUS ERROR
704 1S: MOV @DRVWCR,ACSR ;GET WC
705 BEQ 2S ;IF ZERO
706 MOV @DRVWCR,CSRA ;SET UP WCR ADPS
707 CLR ASTAT ;EXPECTED 0
708 ADD #20,R5 ;POINT TO THE WCR ER
709 RTS R5 ;EXIT HERE ON WCR ER
710 2S: MOV (R5),ASTAT ;GET XFER #
711 ASL ASTAT ;CONVERT TO WORD
712 ADD @BUFFER,ASTAT ;POINT TO LAST XFER #2
713 MOV @DRVBAR,ACSR ;GET B
714 BIC #BIT0,ACSR ;DON'T WANT BIT00
715 CMP ASTAT,ACSR ;COMPARE
716 BEQ 3S ;IF SO
717 MOV @DRVBAR,CSRA ;SET UP BAR ADRS
718 ADD #36,R5 ;POINT TO BAR ER
719 RTS R5 ;EXIT HERE ON BAR ER
720 3S: ADD #54,R5 ;ALL OK - POINT TO GOOD EXIT
721 RTS R5 ;EXIT HERE IF NO ERRORS
```

```
722 ;THIS ROUTINE LOADS "DBUF" WITH A FLOATING ZERO/ONE PATTERN
723 ;FOR 14 LOCATIONS - THE LAST LOCATION IS LOADED WITH THE
724 ;70707 WHICH SHOULD BE THE DATA WORD AVAILABLE IN THE DBR
725 ;AT THE COMPLETION OF A 15 WORD TRANSFER
726 LDBUF: MOV #BUFFER,R3 ;GET BUFFER ADRS
727 1S: MOV R4,(R3)+ ;SET UP FLOATING ZERO PATRN
728 2S: COM R4 ;LOAD IT (FLOATING 0)
729 CME #BUFFER+36,R3 ;WAKE INTO FLOATING 1
730 BR IF NOT
731 3S: MOV #70707,(R3) ;LOAD LAST DATVAR (SPECIAL)
732 RTS PC ;GET OUT
733 4S: MOV R4,(R3)+ ;LOAD IT (FLOATING 1)
734 CME #BUFFER+36,R3 ;AT END OF BUFFER?
735 BR IF SO
736 SEC ;BR IF SO
737 COM R4 ;BACK TO FLOATING ZERO
738 ASL R4 ;SHIFT LEFT
739 INC R4 ;KEEP LSB SET
740 BCC 1S ;GO RESET FLOATING PATRN
741 BR 2S ;GO LOAD NEXT PATRN
742 ;THIS ROUTINE CHECKS 15 LOCATIONS IN "DBUF" FOR GOOD TRANSFERRED
743 ;DATA (#17377) ON "DATO" TRANSFERS - IF AN ERROR IS DETECTED
744 ;THE RETURN IS TO CALL +2 - IF NO ERROR THE RETURN IS TO CALL +4
745 CKDAT: MOV #BUFFER,R1 ;GET BUFFER ADRS
746 1S: MOV #17377,(R1)+ ;DATA OK?
747 BEQ 3S ;BR IF SO
748 MOV DRVDBR,CSRA ;SET UP DRR ADRS
749 MOV #R1,AMAS ;GET ACTUAL DATA XFERED
750 MOV #SBADR ;GET MEMORY ADRS
751 MOV #17377,ASB
752 RTS PC ;RETURN TO ERROR
753 2S: CME #BUFFER+36,R1 ;AT END OF "DBUF"?
754 BNE 1S ;BR IF MORE
755 ADD #4,(SP) ;ADJUST STACK FOR GOOD RETURN
756 PC ;GET OUT
757
758
759
```

```
760 ;THIS ROUTINE LOADS "BUFFER" WITH A UNIQUE FLOATING ZERO/ONE PATTERN
761 ;DATA (17776,0,1,0,17775,0,2,0,17773,0,4,1,17776,1,0,10,0 ETC.)
762 ;IT IS USED WITH MAIN# 614 SET (DATA/DATO SEQUENCE) - 200 LOCs
763 ;ARE LOADED WITH THIS PATTERN
764 LDBUF: MOV #BUFFER,R3 ;GET BUFFER ADRS
765 1S: MOV R4,R5 ;SET IN R5
766 ADD #40,R5 ;POINT TO END OF BUFFER
767 MOV #17776,R4 ;SET UP FLOATING ZERO PATRN
768 2S: MOV R4,(R3)+ ;LOAD IT (FLOATING 0)
769 CLR (R3)+ ;ZERO NEXT
770 COM R4 ;SET UP FLOATING 1
771 MOV R4,(R3)+ ;LOAD IT
772 CLR (R3)+ ;ZERO NEXT
773 CME #R3 ;NO LOCs DONE?
774 BNE 1S ;BR IF NOT
775 3S: RTS PC ;GET OUT
776 COM R4 ;BACK TO FLOATING ZERO
777 ASL R4 ;SHIFT LEFT
778 INC R4 ;KEEP LSB SET
779 BCC 1S ;GO RESET FLOATING PATRN
780 BR 2S ;GO LOAD NEXT PATRN
781 ;THIS ROUTINE CHECK 200 LOCATIONS IN "BUFFER" FOR GOOD TRANSFERRED
782 ;DATA (17776,1,1,1,17775,1,1,17775,2,2,17773,17773,ETC.)
783 ;ON MAIN# MODE TRANSFERS - THE NUMBER OF CHECKS REQUIRED IS INDICATED
784 ;BY THE CALL +2 - IF AN ERROR IS DETECTED THE RETURN IS TO CALL +4 -
785 ;IF NO ERROR THE RETURN IS TO CALL +10
786 CKDAT: MOV #R5,R0 ;GET # OF CHECKS
787 MOV #BUFFER,R2 ;GET BUFFER ADRS
788 1S: MOV #17776,R1 ;SET UP FLOATING ZERO PATRN
789 CLR R1 ;R3 SAYS WHEN TO SHIFT PATRN
790 2S: CME R1,(R2)+ ;DATA OK?
791 BNE 3S ;BR IF NOT
792 CME R1,(R2)+ ;DATA WRITTEN OK?
793 BNE 3S,R0 ;BR IF NOT
794 SUB #2,R0 ;ACCOUNT FOR TWO ADRS'S
795 BGT 4S ;BR IF MORE
796 ADD #6,R5 ;ADJUST FOR GOOD RETURN
797 RTS PC ;GET OUT
798 3S: MOV #R2,AMAS ;SET BAD DATA
799 MOV #R2,SBADR ;GET MEM ADRS
800 MOV #R2,ASB ;LD EXPECTED DATA
801 MOV #RVDBR,CSRA ;SET UP DBR ADRS
802 RTS PC ;RETURN TO ERROR
803 4S: COM R1 ;WOM EXPECT COMPLEMENT
804 COM R3 ;TIME TO SHIFT?
805 BNE 1S ;BR IF NOT
806 ASL R1 ;SHIFT LEFT
807 INC R1 ;KEEP LSB SET
808 BCC 1S ;GO RESET FLOATING PATRN
809 BR 2S ;DO NEXT
810 BUFFER: NOP
811 .BLKW 512.
812 .END
813
```



PUSH = 005746	206#					
PUSH2 = 024646	206#					
RANDS = 04417	206#					
RANMOM = 000054R	190#					
RBUFEA 000130R	189#					
RBUFFA 000126R	191#	417	470	523	564	603
RBUFSZ 000132R	188#					
RBUFVA 000132R	188#					
RESTR1 000264R	168#	241				
RES1 = 000056R	167#	223#				
RES2 = 000056R	167#					
RSTGT = 000112R	167#					
RSTVEC 003140R	46#					
SBADR = 000102R	17#	515	558*	596	637	687#
SEUP2 = 000316R	22#	444*	498*	549*	751*	969*
SEUP3 = 000316R	22#	232				799*
SEUP4 = 000316R	22#	232				
SEVVEC 003124R	41#	485	519	561	600	641
SOPCWI = 000042R	15#					681#
SOPERS = 04440	206#					
SOPFAS 000046R	15#					
SPDINT 000032R	15#					
SPSIZ = 000040	15#					
SR1 = 000016R	14#	199				
SR2 = 000022R	15#					
SR3 = 000022R	15#					
SR4 = 000024R	15#					
START = 000264R	193#	224#				
STAT = 000022R	15#					
SVR0 = 000062R	16#					
SVR1 = 000064R	16#					
SVR2 = 000066R	17#					
SVR3 = 000072R	17#					
SVR4 = 000072R	17#					
SVR5 = 000074R	17#					
SVR6 = 000076R	17#					
SYCNT = 000046R	16#					
TPS = 177524	206#	644				
TRPDPD = 000022	15#					
TSBA = 000476R	425#					
TSBAR = 001746R	425#	365#				
TSCLK = 001352R	388#					
TSDB = 000616R	282#	288#				
TSDBB = 000716R	308#					
TSDT = 001510R	404#	412#				
TSDT0 = 001746R	404#					
TSFAC = 001106R	343#					
TSFAC3 = 001006R	317#	325#				
TSFV = 002710R	541#					
TSMC = 000576R	541#	677				
VECTOR 000010R	144#	234				
WASADR 000104R	176#	445*	499*			
WBUFEA 000136R	193#					
WBUFFA 000134R	193#					
WBUFR0 000140R	194#					
WBUFSZ 000140R	195#					
WDFR = 000116R	185#	225*				

WDTO = 000114R	184#	224*
XFLAG = 000008R	142#	
= 005642R	611#	

. ABS. 000000 000  
005642 001

ERRORS DETECTED: 0  
DEFAULT GLOBALS GENERATED: 0

XDRFB0,XDRFB0/SQL/CRF:SYM-DDXCON,XDRFB0  
RUN-TIME: 1 SECONDS  
RUN-TIME RATIO: 1575=2.6  
CORE USED: 7K (15 PAGES)